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L6	(714/15-57)![CCLS]	8659	L6
L5	(717/120-161)[CCLS]	4086	L5
L4	(717/120-161)![CCLS]	4086	L4

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L3	debug$5 near15 (host$3 or master) and (break or break near1 point$1) and reset$6 and stack near12 debug$5	22	L3
L2	debug$5 near15 (host$3 or master) and (break or break near1 point$1) near15 reset$6	38	L2
L1	debug$5 near15 (host$3 or master)	1365	L1

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1 **Breakpoint debugging mechanism for microprocessor design**
Wang Danghui; Gao Deyuan; Li Tao;
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Pages:456 - 458 Vol.1

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